The Logic of Events, a framework to reason about distributed systems

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January 24, 2012

PRL team

EventML

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Summary

We have :

- A logical specification language (the logic of events) that formalizes the message sequence diagrams systems engineers use.
- A logical and compositional abstraction (event classes) from which we can synthesize code.
- A language (EventML) for defining event classes and their high-level properties.
- Automated tools that prove invariants and derive "inductive logical forms" that streamline the proofs of distributed algorithms.
 - In two days we now construct proofs of agreement and validity properties of a consensus algorithm.
 - Those proofs used to take a month to create.

Proofs as programs \rightarrow Proofs as processes

 Programs are the evidence for Propositions.

Proofs as programs \rightarrow Proofs as processes

- Programs are the evidence for Propositions.
- ► Event ordering = (E, loc(e), info(e), e₁ < e₂) + six axioms
- Event Logic = propositions in CTT about event orderings
- Evidence ?? could be IO-Automata, π-calculus,



. . .

EventML

Event class: the link to computation

An event class X of type class(T) is both

- A relation $v \in X(e)$
 - X observes v at event e
 - ► X associates information v with event e
- A function $X : EO \rightarrow E \rightarrow Bag(T)$

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- $v \in Base(hdr, type)(e) \Leftrightarrow info(e) = \langle hdr, type, v \rangle$

Example: consensus safety properties Agreement

If commands c and c' are chosen for the n^{th} command then c = c'.

Validity Any command decided on must have been proposed.

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Event class combinators

(used here to structure 2/3 majority consensus algorithm)

- main = Replica @ locs
- $\mathsf{Replica} \qquad = \mathsf{NewVoters} \gg = \mathbf{p} \cdot \mathsf{Voter} \mathbf{p}$

Event class combinators

(used here to structure 2/3 majority consensus algorithm)

 ${\sf Replica} \qquad = \ {\sf NewVoters} \ >>= \ {\sf \backslash p} \, . \, {\sf Voter} \ p$

Event classes and combinators are expressible in EventML.

EventML

Computation and logic

Event classes have two facets:

- computational:
 - ▶ they can be implemented as processes (tail recursive)
 - program for each combinator derived from constituent programs
 - all constructions proved correct in Nuprl
 - result: a verified code synthesizer from event classes to processes
- logical:
 - they specify information flow (using the class relation)
 - relation for each combinator derived from constituent relations
 - derived relations proved correct in Nuprl
 - result: a verified translator from event classes to logical relations

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Cooperation with a Logical Programming Environment (LPE)

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EventML prelude

specification rsc4 (* — PARAMETERS — *) (* consensus on commands of aribtrary type Cmd with equality decider *) parameter Cmd, cmdeg : Type * Cmd Deg parameter coeff : Int parameter flrs : Int (* max number of failures *) parameter locs : Loc Bag (* set of exactly (3 * flrs + 1) locations *) parameter clients : Loc Bag (* locations of the clients to be notified *) (* _____ CONSTANTS _____ *) import length poss-maj list-diff deg-member from-upto Memory-class int-list-member (* ------ TYPE FUNCTIONS ------ *) type Inning = Inttype CmdNum = Inttype CI = CmdNum * Inning type CC = CmdNum * Cmd type Vote = (CI * Cmd) * Loc (* ------ *) internal vote : Vote internal retry : CI * Cmd internal decided : CC output notify : CC input propose : CC ▲ロト ▲帰 ト ▲ ヨ ト ▲ ヨ ト ● ● ● ● ●

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EventML

```
(* --- inputs --- *)
let vote2prop loc (((n,i),c), loc') = \{(n,c)\};
class Proposal = propose'base || (vote2prop o vote'base);;
(* --- output --- *)
let when_new_proposal loc (n,c) (max, missing) =
 if n > max or deq-member (op =) n missing then \{(n,c)\} else \{\};
(* --- update --- *)
let update_replica (n,c) (max, missing) =
 if n > max
 then (n, missing ++ (from-upto (max + 1) n))
 else if deq-member (op =) n missing
 then (max, list-diff (op =) missing [n])
  else (max, missing) ;;
(* --- New votes state --- *)
class ReplicaState = Memory-class update_replica (init (0, nil)) Proposal ::
(* --- New votes observer --- *)
class NewVoters = when_new_proposal o (Proposal, ReplicaState) ;;
(* ----- Replica ----- *)
class Replica = NewVoters >>= Voter;;
(* ------ Main program ----- *)
main Replica @ locs ::
```

EventML assertions

```
(* -- state -- *)
class ReplicaState = Memory-class update_replica (init (0,nil)) Proposal ;;
(* -- invariants -- *)
invariant replica_inv on (max, missing) in ReplicaState
= max >= 0
/\ forall x : Int, int-list-member x missing => max > x /\ x > 0;;
```

Automated tactics prove many assertions automatically.

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Inductive logical form (ILF)

automatically generated, automatically proved

```
∀[Cmd:ValueAllType]. ∀[clients:bag(Id)]. ∀[cmdeq:EqDecider(Cmd)]. ∀[coeff,flrs:Z]. ∀[locs:bag(Id)].
∀[es:E0']. ∀[e:E]. ∀[rcvr:Id]. ∀[num.rnd:ℤ]. ∀[c:Cmd]. ∀[sndr:Id].
  (<rcvr, rsc4 vote'msg(Cmd;<<<num, rnd>, c>, sndr>)> ∈ rsc4 Main(e)
  \iff loc(e) \in locs
       \land (rcvr \in locs \land (sndr = loc(e)))
       ∧ (∃e':{e':E| e' <loc e }</p>
            ((∃max:ℤ
                 ∃missing:ℤ List
                  (<\max, \min) \in rsc4 \operatorname{ReplicaState}(\operatorname{Cmd})(e') \land ((\max < \operatorname{num}) \lor (\operatorname{num} \in \operatorname{missing})))
            \land (\exists c':Cmd
                 ((((e = e') \land (c = c') \land (rnd = 0)))
                    ∨ ((∃e1:{e1:E| e1 <loc e })</p>
                           (((∃maxr:ℤ. (maxr ∈ rsc4_NewRoundsState(Cmd) num(e1) ∧ (maxr < rnd)))
                           \land (<<num, rnd>, c> \in rsc4_retry'base(Cmd)(e1)
                             \lor (\existssndr':Id. <<<num. rnd>, c>, sndr'> \in rsc4 vote'base(Cmd)(e1))))
                           \land (e = e1)))
                      ∧ (no rsc4_Notify(Cmd; clients) num between e' and e)))
                 ∧ (<num, c'> ∈ rsc4_propose'base(Cmd)(e')
                   ∨ (∃rnd':Z. ∃sndr':Id. <<<num. rnd'>, c'>, sndr'> ∈ rsc4 vote'base(Cmd)(e')))))))
```

January 24, 2012

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Conclusion

The right abstractions, embedded in a language that can interface with automated theorem provers gives us the ability to synthesize code that provably satisfies high-level specifications.

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