```
add_functionality_wrt_lt  THM

⊢  ∀i1,i2,j1,j2:ℤ. i1<j1 ⇒ i2≤j2 ⇒ i1+i2<j1+j2  by UnivFmlaCD []
|
1. i1 : ℤ
2. i2 : ℤ
3. j1 : ℤ
4. j2 : ℤ
5. i1<j1
6. i2≤j2
⊢  i1+i2<j1+j2  by BackThru Thm* ∀i,j:ℤ. i<j ⟺ i+1≤j    {lt_to_le}
|
⊢  i1+i2+1≤j1+j2  by Assert (i1+1+i2≤j1+j2)
|
.....assertion .....
⊢  i1+1+i2≤j1+j2
  by BackThru
     Thm* ∀i1,i2,j1,j2:ℤ. i1≤j1 ⇒ i2≤j2 ⇒ i1+i2≤j1+j2
     {add_functionality_wrt_le}
END
```